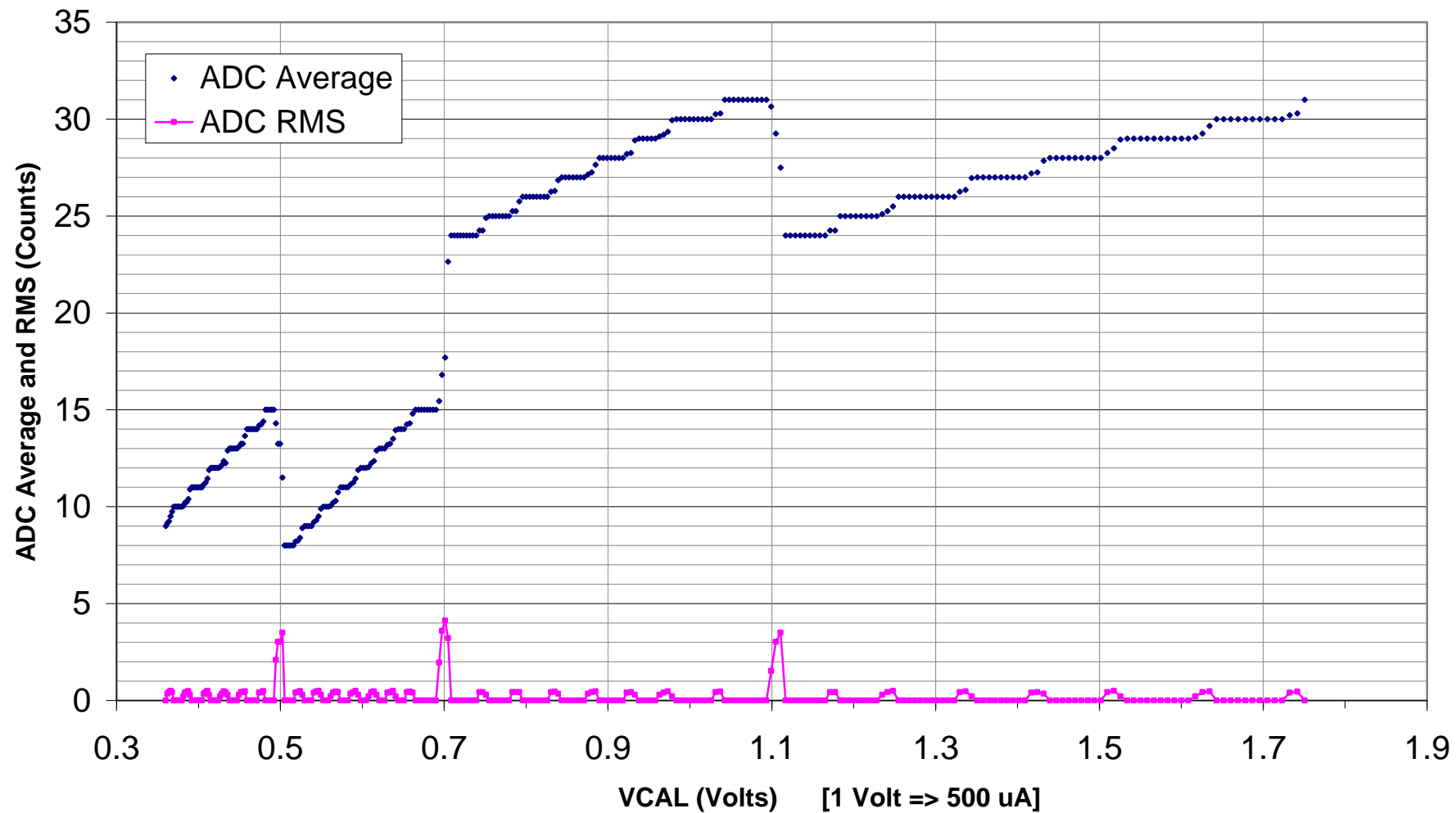


**CMSQIE - ADC Average and RMS**  
**Range 3 Only - Averaged Over CAPIDs**  
Clock Period = 29.3 nS - DC Current Input  
C.N. - 20-Jul-01  
[Note: 8-bit in ADC code is stuck high on testboard]

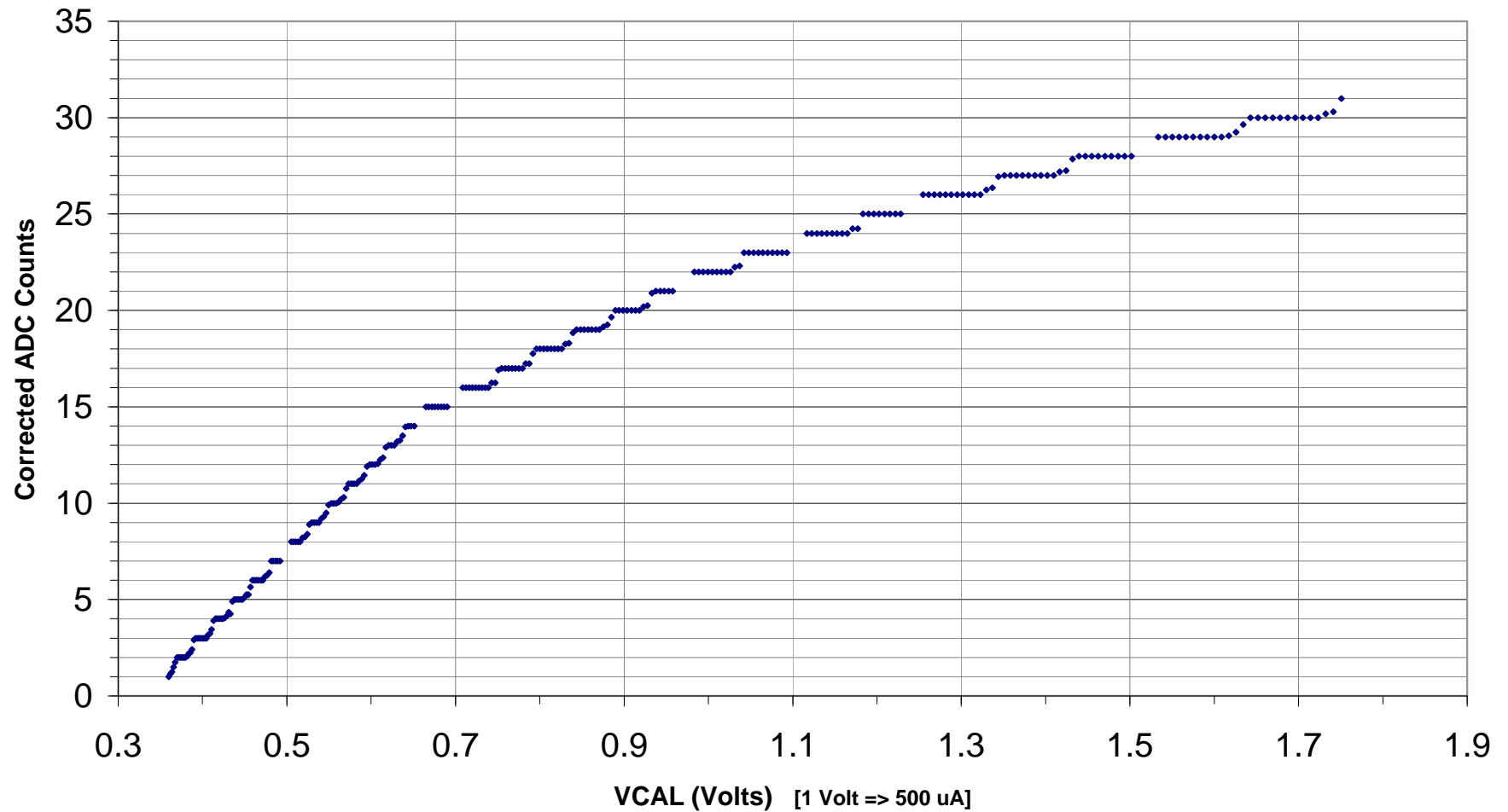


# QIE8 #1 - Corrected for Stuck ADC Bit on Testboard

Range 3 Only - Averaged Over CAPIDs

Clock Period = 29.3 nS - DC Current Input

C.N. - 20-Jul-01

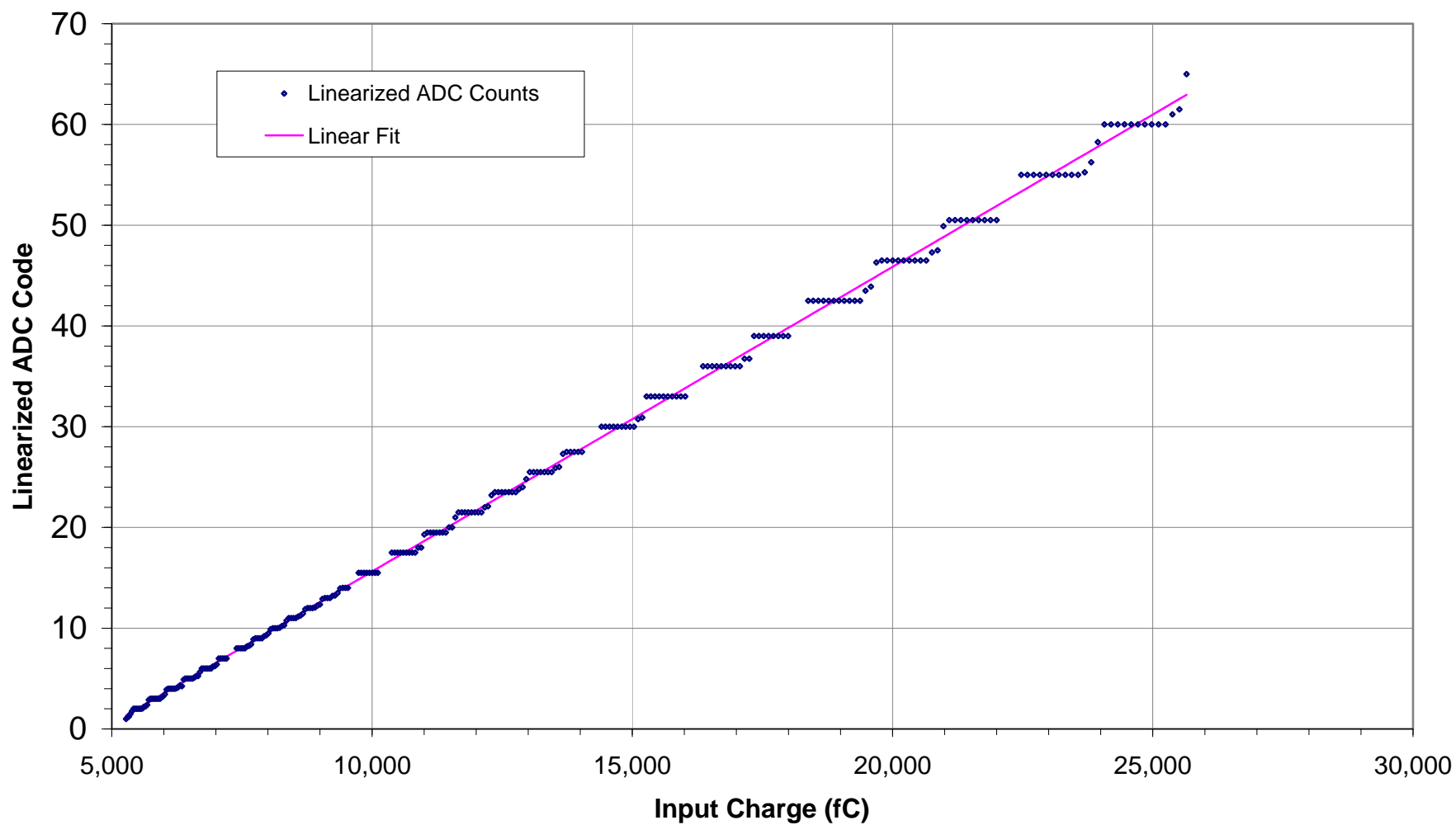


# QIE8 #1 - Linear Fit

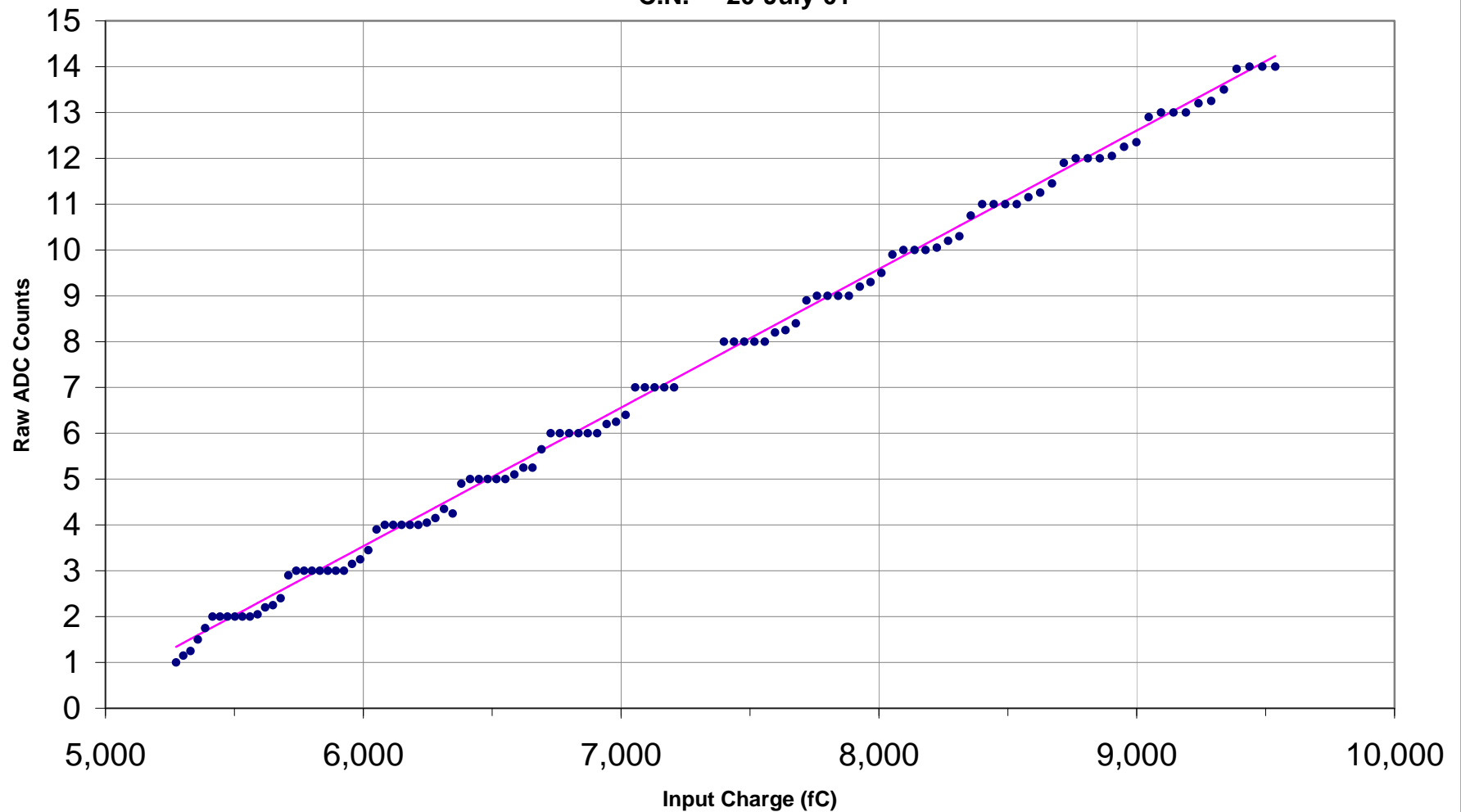
Range 3 Only - Averaged Over CAPIDs

Clock Period = 29.3 nS - DC Current Input

C.N. - 20-July-01



**QIE8 #1 - Linear Fit**  
**Lower End of Range 3 - Averaged Over CAPIDs**  
**Clock Period = 29.3 nS - DC Current Input**  
**C.N. - 20-July-01**



# QIE8 #1 - Fit Residuals

## Range 3 Only - Averaged Over CAPIDs

Clock Period = 29.3 nS - DC Current Input

C.N. - 20-July-01

